

N-Channel 60V(D-S) MOSFET

GENERAL DESCRIPTION

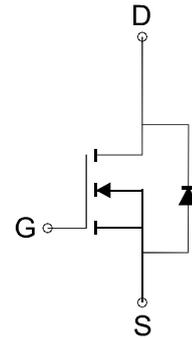
The SK10N06 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance.

APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

FEATURES

- $R_{DS(ON)} \leq 10m\Omega @ V_{GS}=10V$
- $R_{DS(ON)} \leq 13m\Omega @ V_{GS}=4.5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability



N-Channel MOSFET

Absolute Maximum Ratings (T_c=25°C Unless Otherwise Noted)

Parameter	Symbol	Maximum Ratings	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current - Continues	I _D	12.5	A

Electrical Characteristics (T_J=25°C Unless Otherwise Specified)

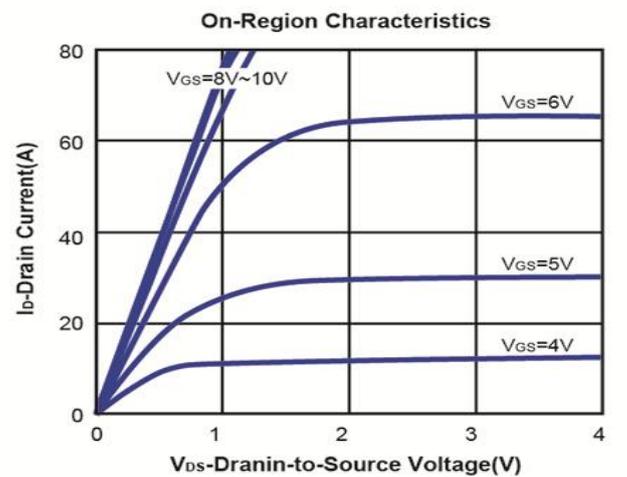
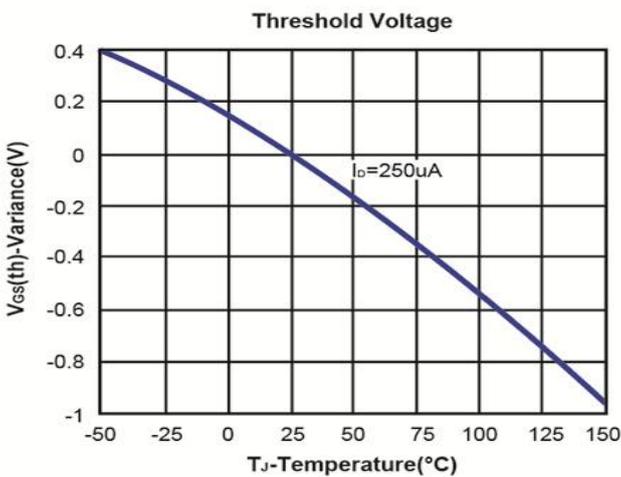
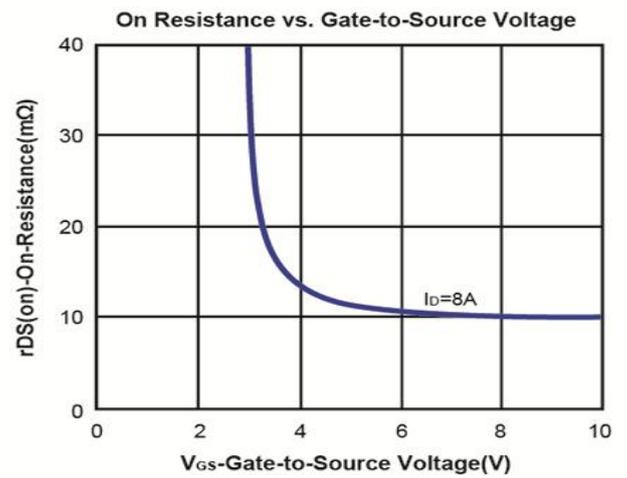
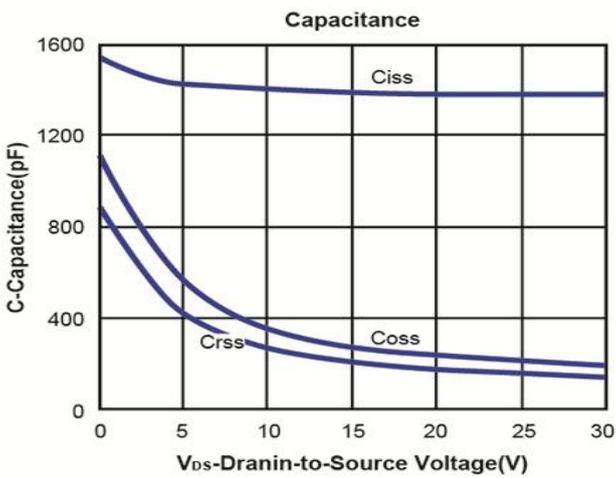
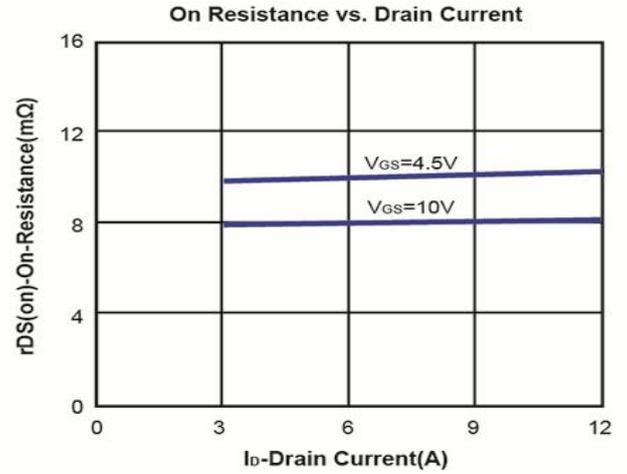
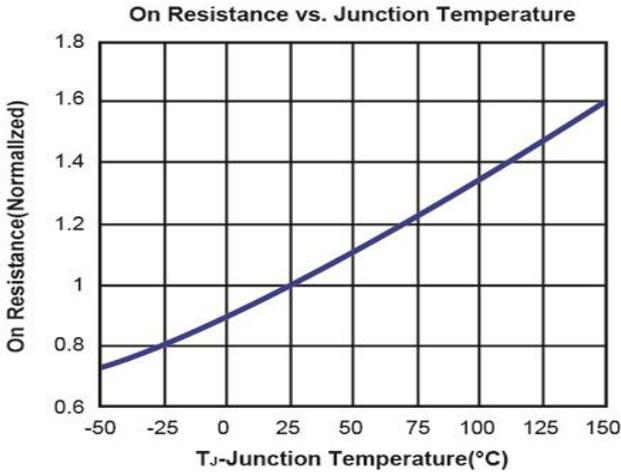
Symbol	Parameter	Limit	Min	Typ	Max	Unit
STATIC						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μA	60			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250 μA	1		3	V
I _{GSS}	Gate-Body Leakage	V _{DS} =0V, V _{GS} =±20V			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =48 V, V _{GS} =0V			1	μA
R _{DS(ON)}	Drain-Source On-Resistance*	V _{GS} =10V, I _D =8A		8	10	mΩ
		V _{GS} =4.5V, I _D =6 A		10	13	
V _{SD}	Diode Forward Voltage *	I _S =8A, V _{GS} =0V,			1.2	V
DYNAMIC						
Q _g	Total Gate Charge	V _{DS} =48V, V _{GS} =4.5V, I _D =8A		46.3		nC
Q _{gs}	Gate-Source Charge			13.9		
Q _{gd}	Gate-Drain Charge			23.7		
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1.0MHz		1386		pF
C _{oss}	Output Capacitance			268		
C _{rss}	Reverse Transfer Capacitance			209		
t _{d(on)}	Turn-On Delay Time	V _{DD} =30V, R _L =15Ω I _D =2A, V _{GEN} =10V R _G =3.3Ω		26.2		ns
t _r	Turn-On Rise Time			19.4		
t _{d(off)}	Turn-Off Delay Time			89.9		
t _f	Turn-Off Fall Time			12.5		

Notes: a. Based on epoxy or solder paste and bond wire Cu 2mil×14(S),2mil×1 (G) on each die of TO-252 package.

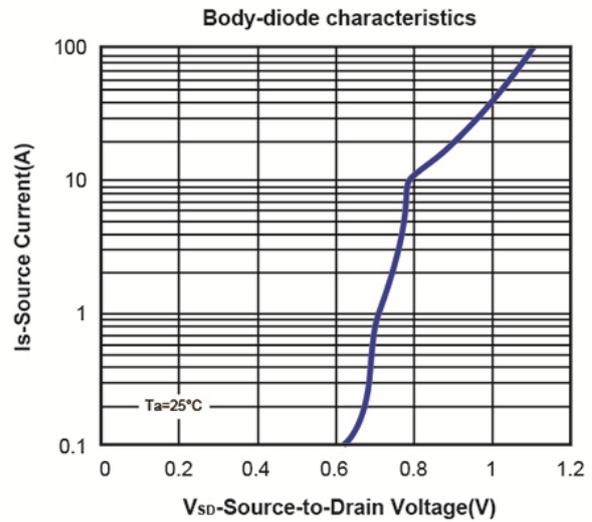
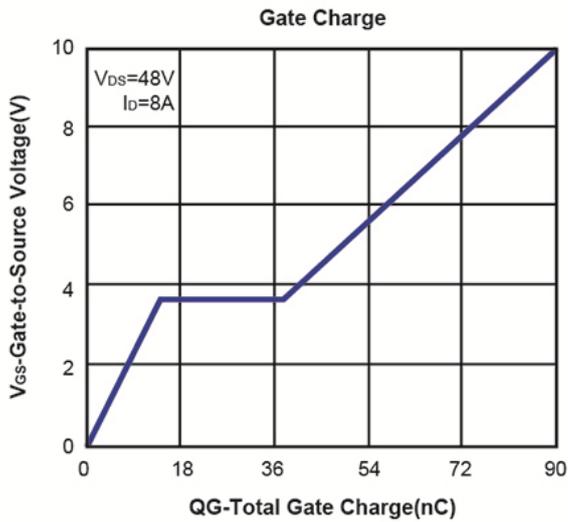
b. Pulse test; pulse width ≤ 300us, duty cycle ≤ 2%.

c. Force mos reserves the right to improve product design, functions and reliability without notice.

Typical Characteristics (T_J = 25°C Noted)

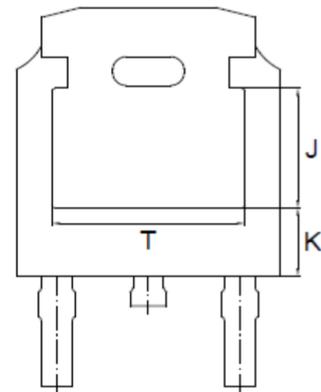
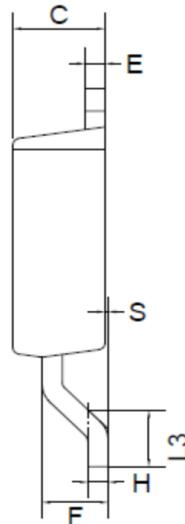
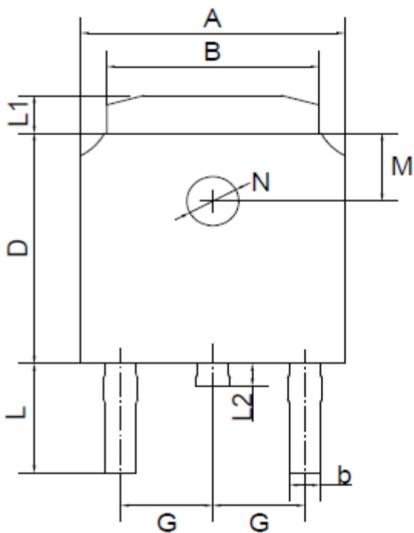


Typical Characteristics (T_J =25°C Noted)



PACKAGE OUTLINE

TO-252(D-PAK)



TO-252(D-PAK) mechanical data

UNIT		A	B	b	C	D	E	F	G	H	L	L1	L2	L3	S	M	N	J	K	T
mm	max	6.7	5.5	0.8	2.5	6.3	0.6	1.8	2.29	0.55	3.1	1.2	1.0	1.75	0.1	1.8	1.3	3.16	1.80	4.83
	min	6.3	5.1	0.3	2.1	5.9	0.4	1.3	TYPICAL	0.45	2.7	0.8	0.6	1.40	0.0	TYPICAL	TYPICAL	ref.	ref.	ref.
mil	max	264	217	31	98	248	24	71	90	22	122	47	39	69	4	71	51	124	71	190
	min	248	201	12	83	232	16	51	TYPICAL	18	106	31	24	55	0	TYPICAL	TYPICAL	ref.	ref.	ref.